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Attorney Docket: 025403-0307457
Client Reference: A2002-410US/SPO200512-0105US

NOV 24 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:

BYEONG RYEOL LEE

Confirmation Number: 2865

Application No.: 10/747,621

Group Art Unit: 2823

Filed: December 30, 2003

Examiner: Quovaunda Jefferson

Title: METHOD OF FORMING ISOLATION STRUCTURES IN EMBEDDED
SEMICONDUCTOR DEVICE

November 24, 2006

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 24, 2006, the date for response to which is extended to November 24, 2006, by the enclosed Petition for a One Month Extension of Time and Fee, please amend the above-identified application as follows:

11/27/2006 TL0111 00000033 033975 10747621
01 FC:1251 120.00 DA